68331/332 *Lite* Application Notes

68331/332 *Lite* units that clip over the target system CPU apply approximately 10 volts to the TSC pin to tristate the CPU pins. Pull up resistors put on the TSC pin should be at least 10K ohms.

The BERR line is used by the *Lite* emulator in programming it's own memory. Target connections to the BERR pin should use a tristate connection or, if pull up resistors are used, they should be at least 10K ohms.

The background port cannot be used by the target system. DSCLK, DSI and DSO are used by the emulator. DSCLK is not connected to the target from the emulator. DSO and DSI should only be monitored by the target system. Pull up resistors put on the DSI or DSO pin should be at least 10K ohms.

Address lines and chip select lines from the *Lite* emulator are unbuffered to the target. The *Lite* emulator will use these lines for programming it's own memory and the target system will see this programming activity.

The *Lite* emulator cannot support fast termination cycles.